APSIPA Social Net and Friend Labs

Kenneth Lam, Thomas Zhang and C.-C. Jay Kuo

One of the key missions of APSIPA is to provide education, research and development exchange platforms for both academia and industry. This mission is being achieved by presentations in APSIPA Annual Summit and Conferences, APSIPA Newsletters, publications in APSIPA Transactions on Signal and Information Processing (an open access journal), etc. Due to the recent emergence of social media such as Facebook and LinkedIn, we have another convenient platform for information sharing. This idea was first discussed in APSIPA ASC 2011 in Xi'an, China, with an overwhelming support from the APSIPA Board of Governors.

Immediately after APSIPA ASC 2011, the APSIPA Social Net program was launched in 2011 December. It is built upon the world most popular professional social network - LinkedIn. A group called "Asia Pacific Signal and Information Processing Association – APSIPA" has been set up in LinkedIn. Any people with a LinkedIn account can apply to join. There are currently around 2500 APSIPA e-members

Furthermore, we establish two types of sub-groups under the APSIPA main group.

• The first type of subgroups is created based on the 6 TC tracks. They are: APSIPA.SPS, APSIPA.SIPTM, APSIPA.SLA, APSIPA.BioSiPS, APSIPA.IVM and APSIPA.WCN. The 6 TC members are encouraged to use their subgroup to communicate

• The second type of subgroups is created based on countries/regions in the Asia-Pacific rim. Now, we have: APSIPA-USA (United States), APSIPA-CHN (China), APSIPA-JPN (Japan), APSIPA-KRO (South Korea), APSIPA-AUS (Australia), APSIPA-CAN (Canada), APSIPA-HKG (Hong Kong), APSIPA-IND (India), APSIPA-NZL (New Zealand), APSIPA-SGP (Singapore), APSIPA -TWN (Taiwan) and APSIPA-THA (Thailand). The country-based subgroup will serve as the basis to build up local chapters.

To enrich contents in the APSIPA Social Net, we have filmed a sequence of interviews. They are accessible via http://www.apsipa.org/social.htm. In 2014 April, this task is assigned to a newly formed APSIPA Social Net Committee (ASNC). The ASNC is led by APSIPA VP-Member Relations and Development, Professor Kenneth Lam, with the following members:

• Ms Summer Jia He, University of Southern California, USA (Secretary)

• Dr Iman Ardekani, Unitec Institute of Technology, New Zealand

• Dr Cheng Cai, Northwest A&F University, China

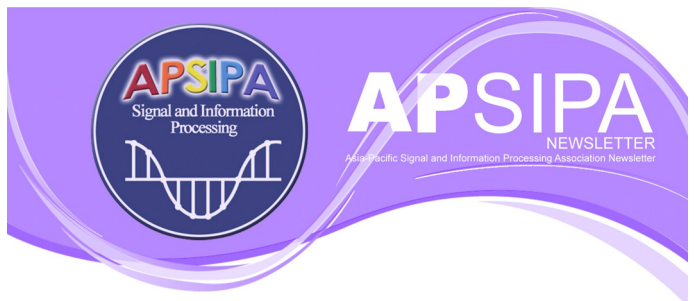• Dr Lu Fang, University of Science and Technology of China

In this issue

- Dr Weiyao Lin, Shanghai Jiao Tong University, China
- Dr Chuang Shi, Kansai University, Japan
- Prof. Chia-Hung Yeh, National Sun Yat-sen University, Taiwan
- Dr Jiantao Zhou, University of Macau, Macau

Another related program called "APSIPA Friend Labs" was launched in 2013 August through the joint effort of APSIPA VP-Institutional Relations and Education, Professor Thomas Zhang, and APSIPA VP-Member Relations and Development, Professor Kenneth Lam. An academia or industrial lab is qualified to be an APSIPA friend lab if it has at least 10 current or former lab members who are full or associate members of APSIPA. A person can be an APSIPA associate member by joining the APSIPA Group in Linkedin. A person can be an APSIPA full member by clicking the "Join Us" button in the up-right corner of the APSIPA homepage and following the given instructions. All APSIPA Friend Labs will be listed in the APSIPA website. Each lab has one page to post lab information, photos and a link to the lab home page. The provided data in the on-line ap-plication form will be used to generate the friend lab page in the APSIPA website. There are about 150 APSIPA Friend Labs now and the number continues to grow. The list of current APSIPA Friend Labs is given in the following page: http://www.apsipa.org/friendlab/Application/lablist.asp.

The APSIPA Social Net and Friend Labs are new initiatives for a professional community. They are still in their early stage, demanding further dedi-cation and innovation to reach the full potential. We sincerely invite you, your friends and col-leagues to join the APSIPA group to become its e-member. We would also like to recruit more research labs to become APSIPA Friend Labs. Your participation will make APSIPA a warm and interactive professional community.

Latest research from the journal:

- **Depth video coding based on intra mode inheritance from texture**, *Elie Gabriel Mora, Joel Jung, Marco Cagnazzo and Béatrice Pesquet-Popescu*

- **A tutorial survey of architectures, algorithms, and applications for deep learning,** *Li Deng*

- **Preamble design for estimation and compensation of channel distortion parameters in OFDM systems**, *Emmanuel Manasseh, Shuichi Ohno and Toru Yamamoto*

- **Nested performance bounds and approximate solutions for the sensor placement problem**, *Muhammad Sharif Uddin, Anthony Kuh, Aleksandar Kavcic and Toshihisa Tanaka*

- **A tutorial survey of architectures, algorithms, and applications for deep learning – ERRATUM**, *Li Deng*

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Recent APSIPA Distinguished Lecturer Activity

1) Lecturer: Tatsuya Kawahara (Kyoto University, Japan)
Title: Recent trend of spoken dialogue systems
Time: 3:00PM, March 26, 2014
Venue: Room 1-303 Info Sci&Tech Building, Tsinghua University, Beijing, China
Host: Prof. Thomas Fang Zheng

There has been significant progress in spoken dialogue systems both in research and deployment. This talk will give a brief history of spoken dialogue systems including their evolution and recent technical trend.
In the new perspective, back-end of spoken dialogue systems is extended from conventional relational databases (RDB) to general documents, incorporating information retrieval (IR) and question-answering (QA) techniques. This paradigm shift and the author's approaches are reviewed.

2) Lecturer: Tatsuya Kawahara (Kyoto University, Japan)
Title: Multi-modal sensing and analysis of poster conversations
Time: 3:00PM, March 28, 2014 (Friday)
Venue: Room 25-B407, Tianjin University, China
Host: Prof. Jianwu Dang

Conversations in poster sessions in academic events, referred to as poster conversations, pose interesting and challenging topics on multi-modal signal and information processing. This talk gives an overview of our project on the smart posterboard for multi-modal conversation analysis. The smart posterboard has multiple sensing devices to record poster conversations, so we can review who came to the poster and what kind of questions or comments he/she made. The conversation analysis combines speech and image processing such as head tracking, speech enhancement and speaker diarization. Moreover, we are also investigating high-level indexing of interest and comprehension level of the audience, based on their multi-modal behaviors during the conversation.
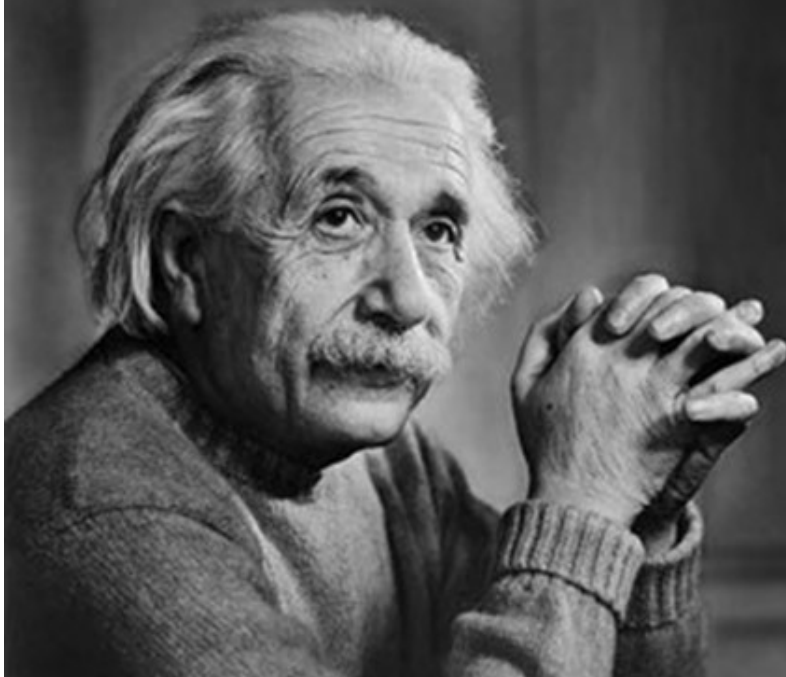
Text-dependent Speaker Verification and RSR2015 Speech Corpus

Anthony Larcher and Haizhou Li

RSR2015 (Robust Speaker Recognition 2015) is the largest publicly available speech corpus for text-dependent robust speaker recognition. The current release includes 151 hours of short duration utterances spoken by 300 speakers. RSR2015 is developed by the Human Language Technology (HLT) department at Institute for Infocomm Research (I2R) in Singapore. This newsletter describes RSR2015 corpus that addresses the reviving interest of text-dependent speaker recognition.

WHY ANOTHER CORPUS?

Speaker verification has reached a maturity that allows state-of-the-art engines to be used for commercial applications. During the last decade, the effort of the community has lead to great improvements in terms of performance and usability of speaker verification engines. Nevertheless, it is well known in the community that performance of text-independent speaker verification engines suffers from the lack of enrolment and training data.

In the context of short duration, text-dependency is known to improve accuracy of speaker verification when dealing with short duration speech segments. Despite its strong commercial potential, text-dependent speaker recognition lies on fringes of the main stream speaker recognition. As a result, the speech resources available for such research are either too small or inadequate to take advantage of the technologies develop for the mainstream text-independent speaker verification. In view of the fact that text-dependent speaker verification and user-customized command and control, that recognizes a user-defined voice command at the same time identifies the speaker, are useful application scenario. RSR2015 is developed for the following objectives.

- To provide a database of reasonable size that supports significance test of text-independent speaker recognition. RSR2015 contains recordings from 300 speakers during 9 sessions in order to create enough target and impostor trials.

- To have a gender-balanced database that allows fair analysis of gender influence in text-dependent speaker recognition. RSR2015 involves 143 female and 157 male speakers.

- To allow for analysis of the phonetic variability in the context of text-constrained speaker recognition [2]. RSR2015 protocol includes more than 60 different utterances spoken by all speakers.

WHAT CAN WE DO WITH RSR2015?

RSR2015 allows for simulation and comparison of different use-cases in terms of phonetic content. For example, the most extreme constraint is to fix a unique utterance for all users of the system all the time. In the case where a larger set of fixed pass-phrases is shared across users, the scenario becomes very similar to user-customized command and control application. On the other hand, it is possible to limit the phonetic content of the speech utterance by randomly prompting sequences of phones or digits. In this case, the context of the phone varies across sessions and especially between enrolment and test.

The choice of a specific scenario depends on what constraints we would like to impose on the users. Unfortunately, no existing database allows for a comparison of speaker recognition engines across scenarios in similar conditions. RSR2015 is designed to bridge the gap. It consists of fixed pass-phrases, short commands and random digit series recorded in the same conditions.

DATABASE DESCRIPTION

RSR2015 contains audio recordings from 300 speakers, 143 female and 157 male in 9 sessions each, with a total of 151 hours of speech. The speakers were selected to be representative of the ethnic distribution of Singaporean population, with age ranging from 17 to 42.

The database was collected in office environment using six portable devices (four smart phones and two tablets) from different manufacturers. Each speaker was recorded using three different devices out of the six. The speaker was free to hold the smart phone or tablet in a comfortable way.

To facilitate the recording, a dialogue manager

was implemented on the portable devices as an Android© application. The speakers interact with the dialogue manager through a touch screen to complete the recording. Each of the 9 sessions for a speaker is organized into 3 parts:

PART 1 – Short-sentences for pass-phrase style speaker verification (71 hours)

All speakers read the same 30 sentences from the TIMIT database [3] covering all English phones. The average duration of sentences is 3.2 seconds. *Example: "Only lawyers love millionaires."*

PART 2 – Short commands for user-customized command and control (45 hours)

All speakers read the same 30 commands designed for the StarHome applications. The average duration of short commands is 2 seconds. *Example "Light on"*

PART 3 – Random digit strings for speaker verification (35 hours)

All speakers read the same 3 10-digit strings, and 10 5-digit strings. The digit strings are session dependent.

INTERSPEECH 2014 SPECIAL SESSION AND BENCHMARKING

During INTERSPEECH 2014, The Institute for Infocomm Research together with IBM Research, and the Centre de Recherche en Informatique de Montreal (CRIM) propose a special session on *Text-Dependent Speaker Verification with Short Utterance*. The purpose of this special session is to gather the research efforts from both the academia and industries toward a common goal of establishing a new baseline and explore new directions for text-dependent speaker verification. For ease of comparison across systems, the RSR2015 database, that comes with several evaluation protocols targeting at different scenarios, has been proposed to support research submitted for the INTERSPEECH 2014 special session [4, 5].

WHERE TO GET THIS DATABASE?

The license is available at [Link]

Please contact Dr Anthony Larcher at Email alarcher [at] i2r.a-star.edu.sg or Dr Kong-Aik Lee at kalee [at] i2r.a-star.edu.sg.

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RECENT ADVANCES IN CONTRAST ENHANCEMENT AND THEIR APPLICATIONS TO LOW-POWER IMAGE PROCESSING

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1. INTRODUCTION

In spite of recent advances in imaging technology, captured images often fail to preserve scene details faithfully or yield poor contrast ratios due to limited dynamic ranges. Contrast enhancement (CE) techniques can alleviate these problems by increasing contrast ratios. Therefore, CE is an essential step in various image processing applications, such as digital photography and visual surveillance.

Conventional CE techniques can be categorized into global and local approaches. A global approach derives a single transformation function, which maps input pixel intensities to output pixel intensities, and applies it to all pixels in an image. Gamma correction, based on the simple power law, and histogram equalization (HE), which attempts to make the histogram of pixel intensities as uniform as possible, are two popular global contrast enhancement techniques [1]. A local approach, on the other hand, derives and applies the transformation function for each pixel adaptively. However, in general, a local approach demands higher computational complexity and its level of CE is harder to control. Therefore, global CE techniques are more widely used for general purposes than local ones. In this article, we review recent advances in global CE techniques and its applications.

2. CE TECHNIQUES AND ITS APPLICATIONS

HE is one of the most popular techniques to enhance low contrast images due to its simplicity and effectiveness. However, it has some drawbacks, such as contrast over-stretching, noise amplification, or contour artifacts. Various researches have been carried out to overcome these drawbacks. For example, several algorithms [2, 3, 4] divide an input histogram into sub-histograms and equalize them independently to reduce the brightness change between input and output images. Recently, histogram modification (HM) techniques, which manipulate an acquired histogram before the equalization, have been introduced. Let us review the generalization of HM algorithms [5], and introduce its applications to low power image processing.

2.1. Histogram Modification

For notational simplicity, let us consider a typical 8-bit imaging system, in which the maximum gray-level is 255. Let $\mathbf{x} = [x_0, x_1, \cdots, x_{255}]^T$ denote the transformation function, which maps gray-level k in the input image to gray-level x_k in the output image [5]. Then, conventional histogram equalization can be achieved by solving

$$\mathbf{D}\mathbf{x} = \overline{\mathbf{h}} \qquad (1)$$

where $\overline{\mathbf{h}} = 255\mathbf{h}/(\mathbf{1}^T\mathbf{h})$ is the normalized input histogram and $\mathbf{D}$ is the differential matrix [4].

HE enhances the dynamic range of an image and yields good perceptual image quality. However, if many pixels are concentrated within a small range of gray levels, the output transformation function may have an extreme slope. This degrades the output image severely. To overcome this drawback, a recent approach to HM modifies the input histogram before the HE procedure to reduce extreme slopes in the transformation function. For instance, Wang and Ward [6] clamped large histogram values and then modified the resulting histogram using the power law. Also, Arici *et al.* [7] reduced the histogram values for large smooth areas, which often correspond to background regions, and mixed the resulting histogram with the uniform histogram. Lee *et al.* [5] employed a logarithm function to reduce large histogram values and prevent the transformation function from having too steep slopes.

In this recent HM approach, the first step can be expressed by a vector-converting operation $\mathbf{m} = f(\mathbf{h})$, where $\mathbf{m}$ denotes the modified histogram. Then, the desired transformation function $\mathbf{x}$ can be obtained by minimizing the cost function

$$C_{\mathrm{H}} = \|\mathbf{D}\mathbf{x} - \overline{\mathbf{m}}\|^2 \qquad (2)$$

where $\overline{\mathbf{m}}$ is the normalized column vector of $\mathbf{m}$.

2.2. Application to Low Power Image Processing

Whereas a variety of techniques have been proposed for the CE of general image, relatively little effort has been made to adapt the enhancement process to the characteristics of display devices. Since a large portion of power is consumed by display panels, it is essential to develop an image processing

(a) Pagoda   (b) Ivy

(c) Lena   (d) F-16

Fig. 1. Comparison of power-reduced output images. In each subfigure, the left image is obtained by the linear mapping method, and the right one by the PCCE algorithm in [5].

(a)   (b)   (c)   (d)

Fig. 2. Brightness-compensated contrast enhancement results on the "Baboon," "Hats," "Building," and "Stream" images at $b = 0.5$. The input images in (a) are compensated by the linear compensation method in (b), the Tsai *et al*.'s algorithm [9] in (d), and the BCCE algorithm [8] in (d).

algorithm, which can save power in display panels as well as enhancing image contrast. Let us introduce display-specific CE algorithms by using the HM framework.

CE for emissive displays The power consumption of emissive displays, such as OLED devices, is proportional to squared pixel intensities [5]. By adding the power consumption term to the initial objective function in (2), we obtain a new cost function

$$C_{\mathrm{E}} = \|\mathbf{Dx} - \overline{\mathbf{m}}\|^2 + \lambda \mathbf{x}^t \mathbf{Hx} \qquad (3)$$

where λ is a user parameter balancing between power and contrast. By minimizing the cost function, we can reduce the power while enhancing the contrast. If $\lambda = 0$, the power saving is not considered. On the other hand, as λ increase, the output image gets darker to achieve power reduction.

Fig. 1 compares the linear reducing method with the proposed power-constrained contrast enhancement (PCCE) algorithm [5]. The linear method provides hazy output images because of the low contrast. On the other hand, the proposed PCCE algorithm yields more satisfactory image qualities.

CE for non-emissive displays Conventional lower power image processing techniques for non-emissive displays, such as TFT-LCD devides, compensate a reduced backlight by increasing pixel intensities. Let us denote the backlight scaling factor as $b \in [0, 1]$. Then, output pixel intensities are scaled by factor of $1/b$. Since the maximum values are limited, the display shows $\min(255, \mathbf{x}/b)$. Therefore, the amount of quality loss at each grey level is given by

$$\mathbf{x}_b = \min(0, \min(255, \mathbf{x}/b) - 255) \qquad (4)$$

The quality loss is modeled by $\mathbf{x}_b^t \mathbf{Hx}_b$ [8], where $\mathbf{H}$ is the diagonal matrix, obtained from the input histogram $\mathbf{h}$. By integrating this term into initial objective function (2), we obtain

$$C_{\mathrm{N}} = \|\mathbf{Dx} - \overline{\mathbf{m}}\|^2 + \mu \mathbf{x}_b^t \mathbf{Hx}_b \qquad (5)$$

where μ is a user-controllable parameter.

Fig. 2 shows the result when the proposed BCCE algorithm [8] for non-emissive displays is applied. In case of the linear mapping, details in bright regions are lost. Although the conventional algorithm in [9] preserves the details more faithfully, the dynamic range is reduced and noise components are amplified. The proposed BCCE algorithm shows better image qualities.

3. CE TECHNIQUES USING HIGHER ORDER STATISTICS

Recently, several algorithms have been proposed to consider the joint distribution of neighboring pixel values for CE. These algorithms exploit the 2D histogram of neighboring pixel values, instead of the 1D histogram of individual pixel values. Celik and Tjahjadi [10] obtained a target 2D histogram by minimizing the sum of the differences from an input 2D histogram and the uniform histogram, and mapped the diagonal elements of the input histogram to those of the target histogram. Lee *et al.* [11] attempted to emphasize the gray-level differences of neighboring pixels that occur frequently in an input image. To this end, they proposed a tree-like data structure, called layered difference representation (LDR), and derived an efficient solution to the optimization problem. Shu and Wu [12] also exploited a 2D histogram of pixel values and computed a transformation function to maximize the expected image contrast. Among these new CE algorithms, let us briefly summarize our LDR work [11].

(a)	(b)	(c)	(d)

Fig. 3. CE results on the "Eagle" and "Night view" image: (a) input image, (b) HE, (c) CVC [10], and (d) the LDR [11].
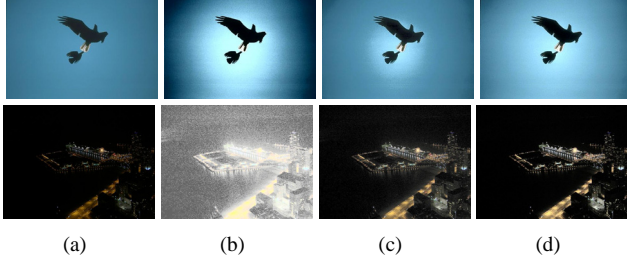
3.1. Layered Difference Representation

In LDR, a 2D histogram is used to find a desirable transformation function $\mathbf{x}$. Suppose that a pair of adjacent pixels in the input image have gray-levels k and $k+l$, then their difference l is mapped to the difference

$$d_k^l = x_{k+l} - x_k, \ \ 0 \le k \le 255 - l \tag{6}$$

in the output image. Let the 2D histogram $h(k, k+l)$ count the number of pairs of adjacent pixels with gray-levels k and $k+l$ in the input image. Also, let $h_k^l = h(k, k+l) + h(k+l, k)$. The objective is to design the transformation function $\mathbf{x}$, which yields a large output difference d_k^l when h_k^l is a large number. In other words, frequently occurring pairs of pixel values in the input image should be clearly distinguished in the output image. To this end, d_k^l should be set proportionally to h_k^l:

$$d_k^l = \kappa_l h_k^l \tag{7}$$

where κ_l is a normalizing constant in [11].

The difference variables, d_k^l's, are grouped according to the input gray-level difference l, and each group is referred to as a *layer* in the hierarchical structure of LDR. Notice that d_k^l can be decomposed into the difference variables d_k^1's at layer 1 by

$$d_k^l = \sum_{i=k}^{k+l-1} d_i^1, \ \ 0 \le k \le 255 - l. \tag{8}$$

Also, the transformation function is determined by the difference variables at layer 1,

$$\begin{aligned} x_0 &= 0, \\ x_k &= \sum_{i=0}^{k-1} d_i^1, \ \ 1 \le k \le 255. \end{aligned} \tag{9}$$

From the relationships in (7) and (8), a difference vector $\mathbf{d} = [d_0^1, d_1^1, \ldots, d_{254}^1]^T$ is determined at each layer. Then, those difference vectors at all layers are aggregated into a unified vector, which is finally used to construct the transformation function $\mathbf{x}$ using (9).

In Fig. 3, HE and CVC cannot handle large histogram properly. Specifically, HE and CVC increase the contrast on the smooth region excessively, producing contour artifacts on "Eagle" image. On the contrary, LDR algorithm [11] exhibits better contrast by exploiting the full dynamic range. Image qualities are degraded when scenes are captured in very low light conditions. "Night view" image indicates a dark input image, which contains noise components. HE yields an extremely noisy image. Although CVC exploits the 2D histogram information, it still experiences the over-enhancement problem due to the high histogram peak. On the other hand, LDR algorithm [11] alleviates noise and clarifies the details of the buildings.

4. CONCLUSIONS

This article introduced the HM framework, formulated as constrained optimization problem. Also, we verified that HM techniques can be applied to the power-constrained image processing algorithms, which can enhance image quality and reduce display power consumption simultaneously. Moreover, we briefly reviewed one of the CE techniques using higher order statistics, which become more popular in recent years.

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Robust Speech Recognition and its LSI Design

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1 Introduction

This letter introduces recent noise robust speech communication techniques and its implementation to a small robot. This speech communication system consists of automatic speech detection, speech recognition and speech rejection.

One of current speech recognition systems are considered as a system consisting of phoneme-based speech recognition and language model [1], [2]. As another speech recognition approach, a phrase-based speech recognition has been also explored [3] - [5]. Compared with the system with phoneme-based speech recognition and language model, the phrase- based speech recognition can provide higher recognition accuracy in case of various noise environments. On the other hand, it requires high calculation cost and a lot of training database for all target speech phrases. By using efficient LSI design of a speech recognition system, the author can realize real time speech recognition with low power consumption. The embedded speech communication module recognizes all target speech phrases with high recognition accuracy where non target speeches are automatically rejected. The smart robot system can answer only for the target speech phrases. About the recognition accuracy of this system, the system can realizes 85% - 99% under 0 - 20 dB SNR and echo environments.

2 Speech Communication System

When we consider an embedded automatic speech recognition (ASR) system, e.g., ROBOT with ASR, many poor surroundings and long distance microphone conditions should be considered. In case of exhibition rooms, out-door, and general house environments, the SNR should be always lower than 20dB. In case of free-hands and general human communications, the 10 cm - 5 m distances should be also considered. The introduced ASR can recognize speech under the above various conditions accurately. The conditions used in our development are considered as (1) white noise,
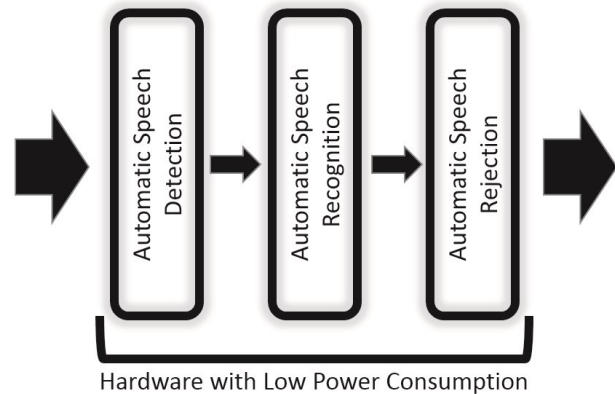
Figure 1: Our Speech Communication System

speech-like noise and factory noise with 5dB SNR, (2) echo environment less than 150ms and (3) medium distance ($< 5m$) from a microphone.

In order to keep the high performance of ASR, our system only recognizes the set of selected phrases. Any other words and phrases except target speech are rejected by our system. In Fig.1, the overview of our speech communication system is described. The system consists of automatic speech detection, i.e., automatic voice activity detection (VAD), automatic speech recognition (ASR), and automatic speech rejection.

The ASR in this system has been designed as a noise robust ASR. In order to make the system robust against various noises, the combination of both Dynamic Range Adjustment (DRA) and Running Spectrum Filtering (RSF) has been developed [3] - [5]. The noise-biased speech features can be compensated by using these methods and thus its recognition accuracy can be improved under many noise circumstances.

The automatic speech rejection has been used for the elimination of speech-like sounds and non-target speech phrases. The ASR in this system only concentrates a target phrase, e.g., a command word, and thus the set of these command words has a limited number of phrases. The system considers they are target and registered into the dictionary. Any other phrases are regarded as the out-of-dictionary words and they are rejected by this system. This mechanism has been realized

Figure 2: Block Diagram of Speech Communication System

by using several criterions for the likelihood values observed from HMM in the recognition stage. These criterions are applied for each target phrase and the optimum conditions are determined by using the speech database used in a training stage.

3 LSI Design of Speech Communication System

As another important property in human-machine interface, a response time should be considered. When a general human interaction is considered, 100—200msec responses can keep a real-time response. In other words, if the system can respond its answer to a user within 150msec, a user feels its response becomes real-time. Compared with other real-time processing, i.e., parallel/pipeline computer, wireless communication and multi-media machine to machine communications, the time period of 150msec seems to be slow and thus it is not difficult to design such systems. However, when we would like to design the system with low power consumption and real-time processing at the same time, its design becomes complicated. In order to realize both of them, we have developed a full custom LSI for our speech communication system. Fig.2 shows its block diagram.

Environment	Noise Level	Accuracy
Meeting Room	50 dB	96.4%
Elevator	50 dB	95.0%
Stairs	45 dB	85.1%
Car A (Idling, No-Moving)	50 dB	99.4%
Car B (High Speed, Open Window)	75 dB	93.3%
Car C (High Speed, Audio ON (F M))	75 dB	88.9%

Figure 3: Accuracy of Speech Communication

The architecture consists of HMM calculation module, noise robust processing module, and MFCC calculation module. In addition, the master and slave connections are given through BUS CONTROL and thus several same LSIs can be connected in a system.

The first designed LSI was fabricated with Rohm CMOS 0.35 m in 2000. It can respond its recognition result within 0.180msec/phrase, 10MHz clock and 93.2mW power consumption where 1,000 phrases are recognized by using this LSI. The real time processing has been realized for 1,000 target phrases. Using parallel connections of these LSIs, the system can recognize more than 1,000 phrases within the same response time. Using this LSI, a small ASR evaluation board has been developed. Its specification is as follows:

1. Phrase ASR system where 1,000 maximum number of phrases can be registered.
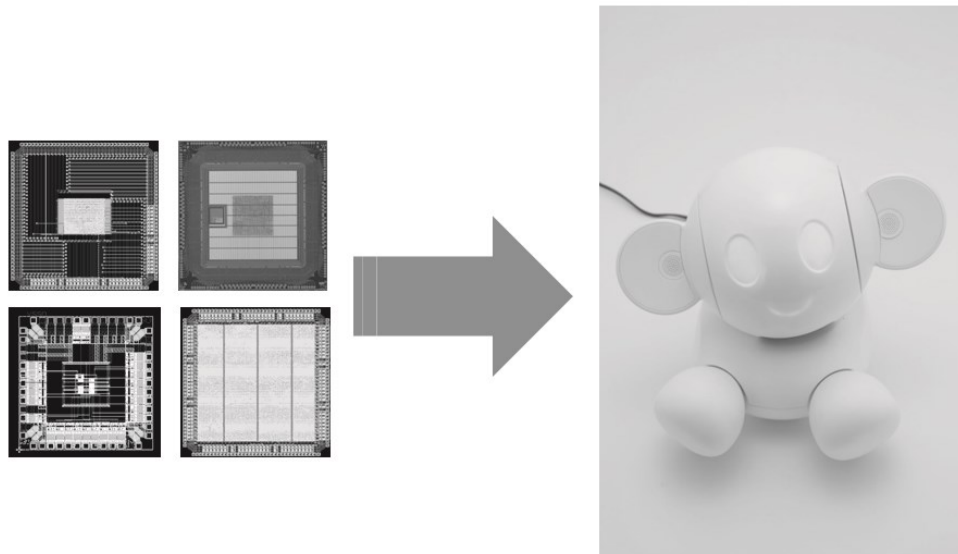
Figure 4: Chapit

2. FLASH memory of NOR type 32 bit and 16 bit ADC with at most 44.4 kHz sampling frequency are embedded.

3. The host connections are SPI, I2C, and UART.

4. The maximum clock is 22.5792 MHz and 3.3v power voltage is supplied. The 180 msec response time can be realized with 10MHz clock.

5. The small scale size is L: 55mm x W: 44mm x H: 12mm.

Using the evaluation board, the results of the field experiments are shown in Fig.3. In these experiments, the microphone distances are 30cm, 60cm and 90cm where these distances are generally required by many users. The total number of phrases is 15. For the experiments on echo robustness, the circumstances on a meeting room, an elevator and a stairs hall have been used. For the experiments of noise robustness, the circumstances on three different conditions of cars have been used.

As the total performance on echo and noise circumstances, the developed ASR system can realize 93.0% correctness (speech recognition accuracy and rejection accuracy). When we use this system in exhibitions, the system can recognize all words under the condition on 70 - 75 noisy level and 2 - 5 m microphone distance.

4 Speech Communication Module Embedded Smart Info-media System

The introduced speech communication module is embedded into a small robot. It is shown in Fig.4. The total system of this small robot has been designed by Raytron [6]. The two microphones at the ear of this robot are integrated and thus a sound in front of this robot is mainly recorded and analyzed.
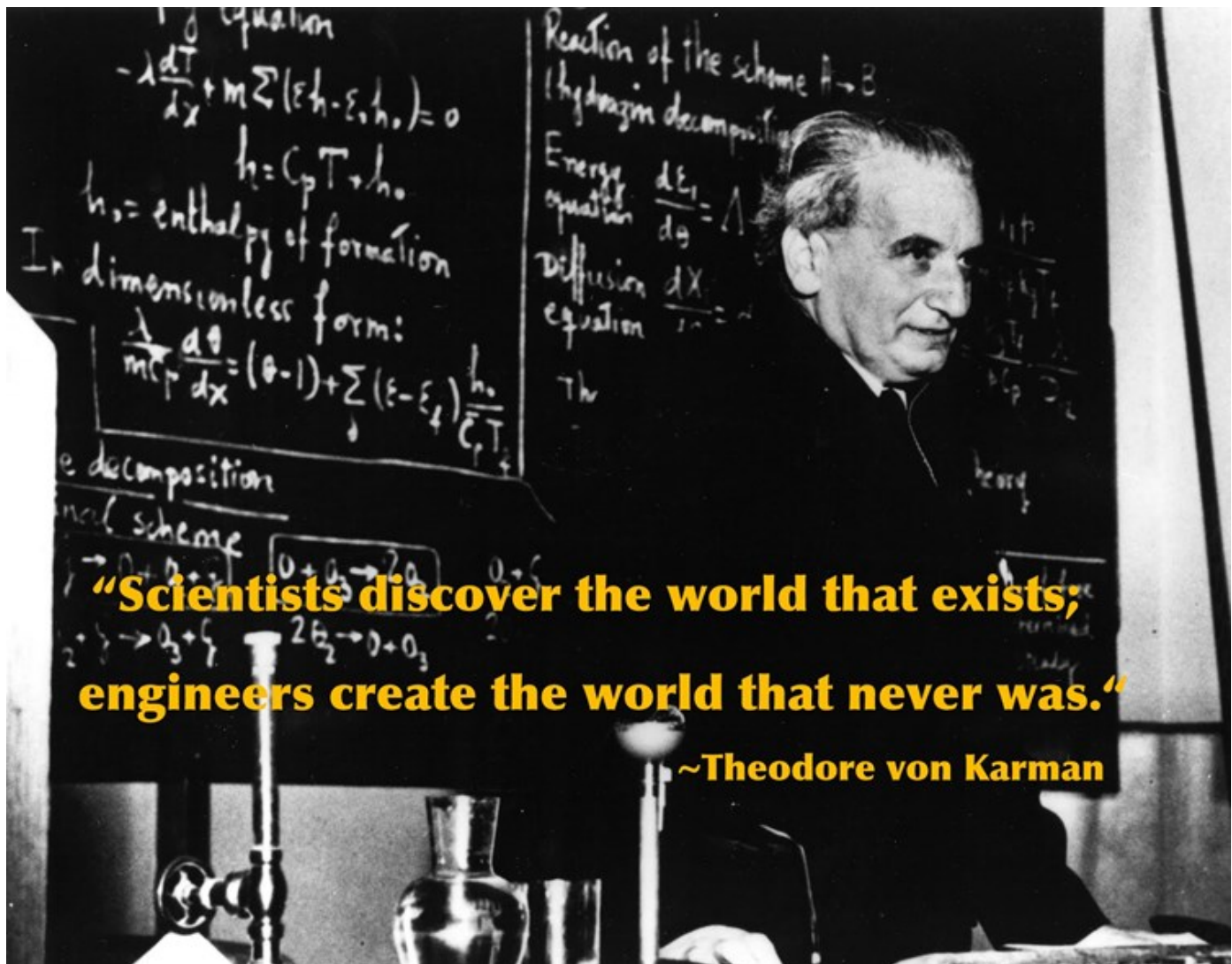
The name of this robot is CHAPIT and it was announced in the late year of 2007. It can recognize only 300 words at the first time. However, a user can speak any words against the robot with 30cm—5m distance and 5 - 30 dB noisy/echo circumstances and then the robot answer for the registered phrases. Its interface style seems to be quite familiar among human to human communications.

5 Conclusion

In this letter, a speech communication system has been introduced. It can provide high accuracy of speech recognition under noisy and echo circumstances. The robust techniques are implemented into automatic speech detection, automatic speech recognition and automatic speech rejection. As current speech recognition accuracy, the system can realize 85% - 99% under 0 - 20 dB SNR and 150msec echo environments.

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"Scientists discover the world that exists; engineers create the world that never was."
~Theodore von Karman

- Book Review –

The Road to Scientific Success:
Inspiring Life Stories of Prominent Researchers

Edited by **Deborah D L Chung** (University at Buffalo, State University of New York, USA); email: ddlchung@buffalo.edu

This book series describes the road to scientific success, as experienced and described by prominent researchers. The focus on research process (rather than research findings) and on personal experience is intended to encourage the readers, who will be inspired to be dedicated and effective researchers.

The objectives of this book series include the following.

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• To help parents and teachers prepare the next generation of scientists or engineers

• To increase the awareness of the general public to the advances of science

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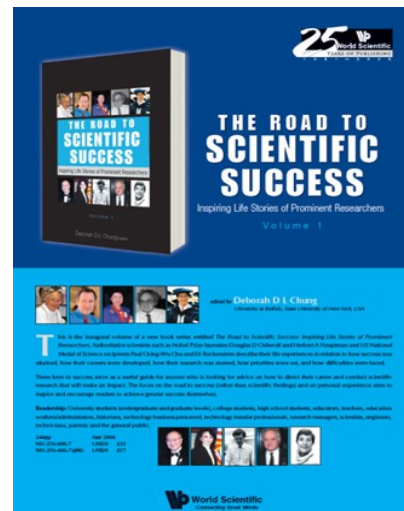
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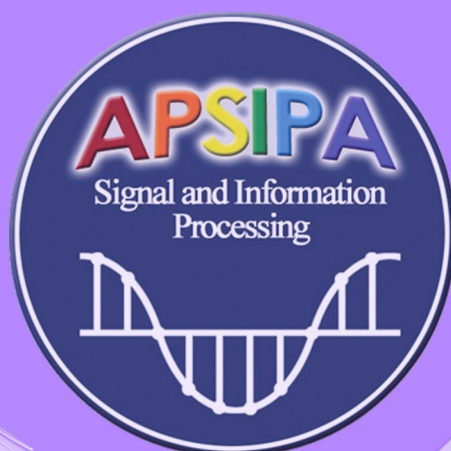
Asia-Pacific Signal and Information Processing Association Annual Summit and Conference 2014

December 9-12, 2014, Chiang Mai, Thailand

Call for Papers

Welcome to the APSIPA Annual Summit and Conference 2014 located in Chiang Mai, the most culturally significant city in northern Thailand. Chiang Mai is a former capital of the Kingdom of Lanna (1296-1768) and is well known of historic temples, arresting scenic beauty, distinctive festivals, temperate fruits and invigorating cool season climate. The sixth annual conference is organized by Asia-Pacific Signal and Information Processing Association (APSIPA) aiming to promote research and education on signal processing, information technology and communications. The annual conference was previously held in Japan (2009), Singapore (2010), China (2011), USA (2012) and Taiwan (2013). The field of interest of APSIPA concerns all aspects of signals and information including processing, recognition, classification, communications, networking, computing, system design, security, implementation, and technology with applications to scientific, engineering, and social areas.

The regular technical program tracks and topics of interest include (but not limited to):

1. Biomedical Signal Processing and Systems (BioSiPS)
1.1 Biomedical Imaging
1.2 Modeling and Processing of Physiological Signals (EKG, MEG, EKG, EMG, etc.)
1.3 Biologically-inspired Signal Processing
1.4 Medical Informatics and Healthcare Systems
1.5 Genomic and Proteomic Signal Processing

2. Signal Processing Systems: Design and Implementation (SPS)
2.1 Nanoelectronics and Gigascale Systems
2.2 VLSI Systems and Applications
2.3 Embedded Systems
2.4 Video Processing and Coding
2.5 Signal Processing Systems for Data Communication

3. Image, Video, and Multimedia (IVM)
3.1 Image/video Coding
3.2 3D image/video Processing
3.3 Image/video Segmentation and Recognition
3.4 Multimedia Indexing, Search and Retrieval
3.5 Image/video Forensics, Security and Human Biometrics
3.6 Graphics and Animation
3.7 Multimedia Systems and Applications

4. Speech, Language, and Audio (SLA)
4.1 Speech Processing: Analysis, Coding, Synthesis, Recognition and Understanding
4.2 Natural Language Processing: Translation, Information Retrieval, Dialogue
4.3 Audio Processing: Coding, Source Separation, Echo Cancellation, Noise Suppression
4.4 Music Processing

5. Signal and Information Processing Theory and Methods (SIPTM)
5.1 Signal Representation, Transforms and Fast Algorithms
5.2 Time Frequency and Time Scale Signal Analysis
5.3 Digital Filters and Filter Banks
5.4 DSP Architecture
5.5 Statistical Signal Processing
5.6 Adaptive Systems and Active Noise Control
5.7 Sparse Signal Processing
5.8 Signal Processing for Communications
5.9 Signal Processing for Energy Systems
5.10 Signal Processing for Emerging Applications

6. Wireless Communications and Networking (WCN)
6.1 Wireless Communications: Physical Layer
6.2 Wireless Communications and Networking: Ad-hoc and Sensor Networks, MAC, Wireless Routing and Cross-layer Design
6.3 Wireless Networking: Access Network and Core Network
6.4 Security and Cryptography
6.5 Devices and Hardware

Submission of Papers

Prospective authors are invited to submit either full papers, up to 10 pages in length, or short papers up to 4 pages in length, where full papers will be for the single-track oral presentation and short papers will be mostly for poster presentation. The conference proceedings of the main conference will be published, available and maintained at the APSIPA website.

Important Dates

Submission of Proposals for Special Sessions, Forum, Panel & Tutorial Sessions	May 9, 2014
Submission of Full and Short Papers	June 6, 2014
Submission of Papers in Special Sessions	July 4, 2014
Notification of Papers Acceptance	Aug. 29, 2014
Submission of Camera Ready Papers	Sep. 26, 2014
Author Registration Deadline	Sep. 26, 2014
Tutorial Session Date	Dec. 8, 2014
Summit and Conference Dates	Dec. 9-12, 2014

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19th DSP 2014

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20-23 August 2014   dsp2014.org

The 2014 19th International Conference on Digital Signal Processing (DSP 2014), sponsored by Imperial College, London, and co-sponsored by the IEEE Signal Processing Society, the Hong Kong Polytechnic University, EURASIP, APSIPA, and IET, will be held in August 20-23, 2014 in Hong Kong. It is the longest in existence Conference in the area of DSP and belongs to a series of events, which commenced in London in 1968 and continued to Florence, Nicosia, Limassol, Santorini, Cardiff, Corfu. The last meeting took place in Santorini, Greece in July 2013. DSP 2014 addresses the theory and application of filtering, coding, transmitting, estimating, detecting, analysing, recognising, synthesising, recording, and reproducing signals by means of digital devices or techniques. The term "signal" includes audio, video, speech, image, communication, geophysical, sonar, radar, medical, musical, and other signals.

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April 21, 2014

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May 21, 2014

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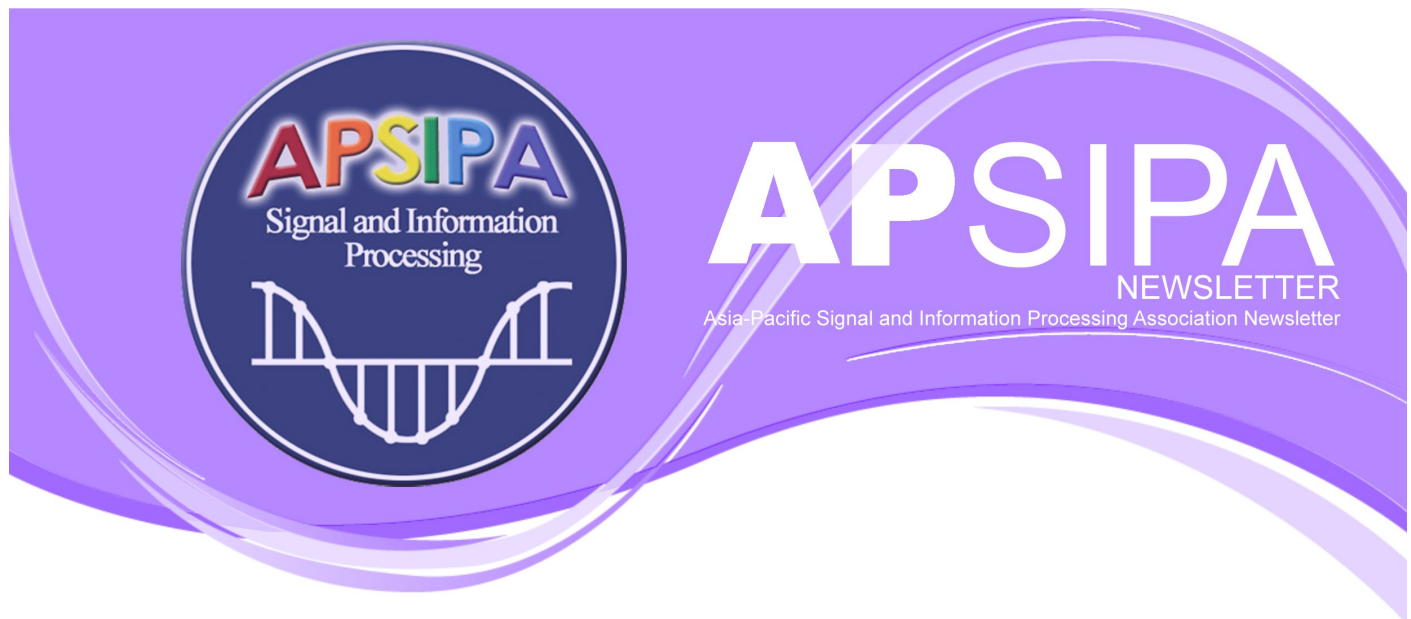Manager: Kin-Man Lam, Kenneth,
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