

Minutes of Circuits, Design and Implementation TC meeting

Date: 12:10-13:10 on Dec. 16, 2010

Place: Meeting room 1, Matrix, Biopolis, Singapore

Attendee: Prof. Akinori Nishihara (Tokyo Institute of Tech.), Prof. Sanjit K. Mitra (USC), Prof. Takao Nishitani (Tokyo Metropolitan Univ.), Prof. Yoshikazu Miyanaga (Hokkaido Univ.), Prof. Jun Han (Fudan Univ.), Prof. Constantin Siriteanu (Hokkaido Univ.), Dr. Kosin Chamnongthai (King Mongkut's University of Technology Thonburi), Prof. Takeshi Ikenaga (Waseda Univ.)

Agenda:

1. TC name and abbreviation

TC name candidate:

Signal Processing Systems: Design and Implementation

Abbreviation:

SPS

Others: Technical objective of APSIPA is unclear. We will confirm it to BoG and discuss again via e-mail if needed.

2. Technical Scope and Key Words of our TC

Key words:

Analog/Digital Systems

Hardware/Software Systems

Media/Communication Processing Systems

etc.

3. Bylaws

We should confirm the relation between the TC of APSIPA and the TPC of ASC in order to make clear our role.

4. TC structure and recruitment of other TC members

New member:

Prof. Constantin Siriteanu from Hokkaido Univ (recommended by Prof. Miyanaga)

Recruitment:

Someone from Taiwan.

Continue recruiting appropriate members via e-mail.

5. Activity plan for ASC 2011

Special session:

Many core processor/Supercomputer based on GPGPU.

Try to invite someone who engages in research on the No. 1 supercomputer in China.

(or TUBAME from TIT)

Ask Prof. Jun Han (and Prof. Zhenyu Liu) to organize this session.

Ask Prof. Nishitani to decide an appropriate title and scope.

Try to arrange one more special session via e-mail.